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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## BOX PATENT APPLICATION

The Commissioner of Patents and Trademarks P.O. Box 1450, Alexandria, Virginia 22313-1450

Sir:

Transmitted herewith for filing is a **new utility** patent application of: Chung-Hung LIN et al.

## Title of Invention: INTEGRATED CIRCUIT PACKAGING FOR IMPROVING EFFECTIVE CHIP-BONDING AREA

Enclosed are:

A specification and 18 claims.

Six (6) sheets of formal drawings (Figs. 1-6).

A Declaration and Appointment of Attorney

Claim for Priority and Priority Document

An Assignment to: ChipMOS Technologies (Bermudea) Ltd. AND ChipMOS TECHNOLOGIES INC.

Information Disclosure Statement along with PTO-1449 and One (1) reference.

The filing fee has been calculated as shown below:

|              |                          | <u>SMALL</u>  | <u>LARGE</u>  |
|--------------|--------------------------|---------------|---------------|
| FOR: NO. F   | FILED NO. EXTRA          | <b>ENTITY</b> | <b>ENTITY</b> |
|              |                          | RATE FEE      | RATE FEE      |
| BASIC FEE    |                          | \$385.00      | \$770.00      |
| TOTAL CLAIMS | <b>18-</b> 20 = <u>0</u> | \$ 9          | \$18          |
| INDEP CLAIMS | <b>3</b> - 3 = <u>0</u>  | \$ 43         | \$86          |
| MULTIPLE DE  | PENDENT CLAIMS           | \$145.        | \$290.        |
|              |                          |               |               |
|              | TOTAL                    | \$            | \$770.00      |

- X A check in the amount of \$810.00 to cover the government filing fee and the assignment recording fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 CFR 1.16 and 37 CFR 1.17 or credit any overpayment to Deposit Account No. 501874.

Date: November 26, 2003

Bruce H. Troxell

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